



European Hybrid IBIS Summit with IEEE SPI 2023

May 10, 2023

Agenda

All times WEST

- 14:00 **SIGN IN**
- 14:10 **Welcome**
Randy Wolff (Siemens EDA, USA)
(Chair, IBIS Open Forum)
- 14:15 **IBIS Chair's Report**
Randy Wolff (Siemens EDA, USA)
(Chair, IBIS Open Forum)
- 14:30 **Variability-Aware Modeling of Supply Induced Jitter in CMOS Inverters**
Vinod Kumar Verma (India Institute of Technology, India)
Jai Narayan Tripathi (India Institute of Technology, India)
[Presented by Vinod Verma]
- 15:00 **LIM - A General Purpose Simulator for High-Speed Circuit Design**
José Schutt-Ainé (University of Illinois, USA)
Thong Nguyen (Synclisis, USA)
Daniel Shaw (Synclisis, USA)
[Presented by José Schutt-Ainé]
- 15:30 **Touchstone Immediate and Long-Term Future**
Michael Mirmak (Intel Corp., USA)
- 16:00 **BREAK (15 minutes)**
- 16:15 **An accurate IBIS modelling technique for Open Drain Drivers**
Rahul Kumar (STMicroelectronics, India)
Manish Bansal (STMicroelectronics, India)
Raushan Kumar (STMicroelectronics, India)
[Presented by Rahul Kumar]
- 16:35 **A Novel Simulation Flow for DDR5 Systems with Clocked Receivers**
Matthew Leslie (Siemens EDA, USA)
Justin Butterfield (Micron Technology, USA)
Randy Wolff (Siemens EDA, USA)
[Presented by Randy Wolff]

- 17:15 **IBIS Quality Checklist Level 4 Additions**
Weston Beal (Siemens EDA, USA)
- 17:55 **CLOSING REMARKS**
- Next IBIS Open Forum Meeting Friday, May 12, 2023
- 18:00 **END OF SUMMIT**
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